



POWER
<b>M8128A</b>

## POWER FACTOR CORRECTION CONTROLLER

### GENERAL DESCRIPTION

The M8128A provides simple and high performance active power factor correction. The M8128A provides a controlled on-time to regulate the output DC voltage and achieve natural power factor correction. The maximum on-time of the external switch is programmable to ensure safe operation during AC brownouts. An innovative multi-vector error amplifier is built in to provide rapid transient response and precise output voltage clamping. A built in circuit will disable the controller if the output feedback loop is opened.

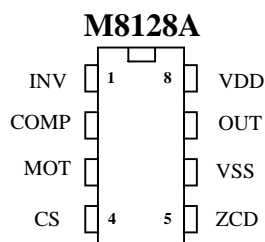
### FEATURES

- Low Input Current THD
- Controlled On-Time PWM
- Zero-Current Detection
- Leading-Edge Blanking instead of RC Filtering
- Low Start-up Current
- Low Operating Current
- Transition mode operation
- Under Voltage Lockout with Hysteresis
- Feedback Open Loop Protection
- Programmable Maximum On-Time
- 8-pin DIP or 8-pin SOP

### APPLICATIONS

- Electronic Lamp Ballasts
- AC-DC Switching Mode Power Converters
- Open Frame Power Supplies and Power Adapters
- Flyback Power Converters with ZCS/ZVS

### PIN ASSIGNMENT

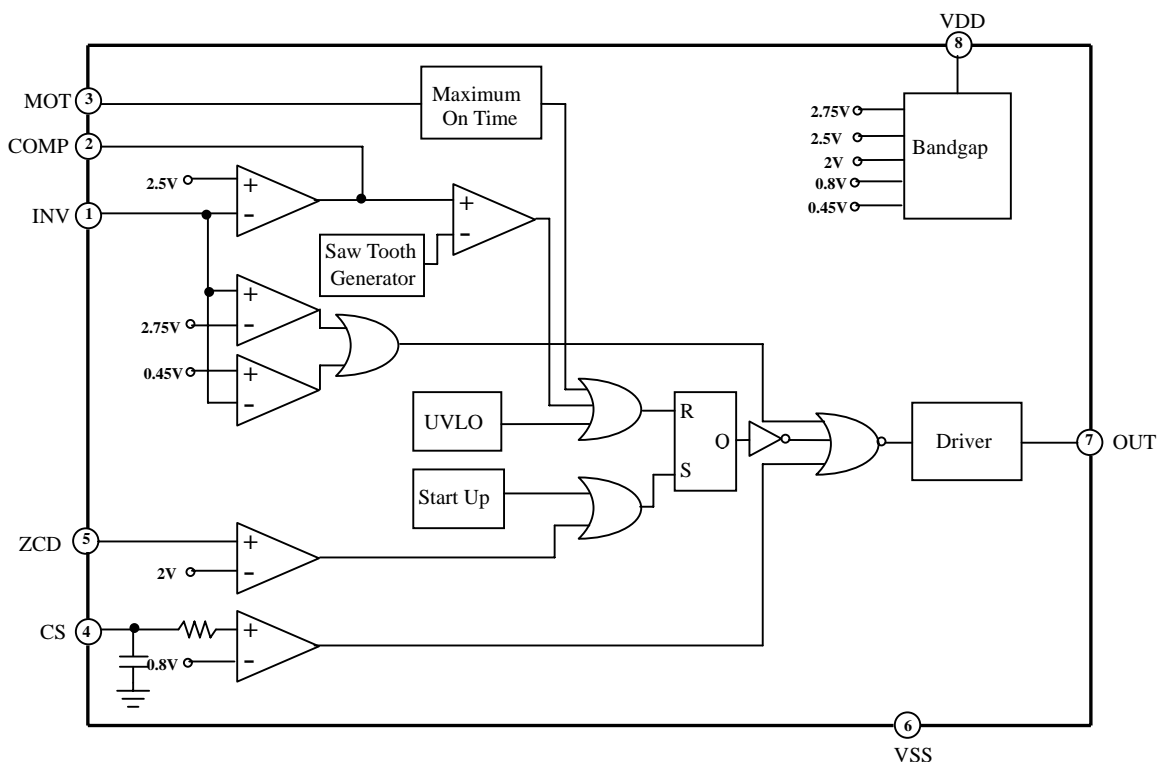




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**BLOCK DIAGRAM**



**PIN DESCRIPTION**

Pin No	Pin Name	Description
1	INV	Inverting input of the error amplifier. INV is connected to the converter output via a resistive divider. This pin is also used for over-voltage clamping and open loop feedback protection.
2	COMP	The output of the error amplifier. In order to create a precise clamping protection, a compensation network between this pin and VSS is suggested.
3	MOT	A resistor from MOT to VSS is used to determine the maximum on-time of the external power MOSFET. The maximum output power of the converter is a function of the maximum ON-time.
4	CS	Input to the over-current protection comparator. When the sensed voltage across the sense resistor reaches the internal threshold, the switch will be turned off to activate cycle-by-cycle current limiting.
5	ZCD	Zero current detection input.
6	VSS	The ground potential of all the pins.
7	OUT	Totem-pole Gate driver output. The push pull output stage is able to drive the Power MOSFET with peak current of 500mA.
8	VDD	Supply voltage of driver and control circuits.



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### ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Condition	Value	Unit
Supply Voltage	VDD		15	V
OUT	VHIGH		-0.3 to 14	V
Others (INV, COMP, MOT, CS,)	VLOW		-0.3 to 7	V
Input Voltage to CS Pin	VCS		-0.3 to 14.7	V
Power Dissipation	PD	at TA < 50°C	800	mW
Storage Temperature Range	TSTG		-65 to +150	°C

\* All voltage values, except differential voltages, are given with respect to VSS pin.

\* Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device.

### ELECTRICAL CHARACTERISTICS

(VDD= 12V, TA = -20°C~125°C, unless noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Under Voltage Lockout Section</b>						
Continuously Operating Voltage	VOP			15	18	V
Start Threshold Voltage	Vth (st)	VDD increasing	6.0	6.5	7.0	V
UVLO Hysteresis	HY (st)	--		1.5	2	V
<b>Supply Current Section</b>						
Start-Up Current	ICC-ST	VDD= VTH-ON -0.16V		180	200	uA
Operating Supply Current	ICC	Output not switching	--	3	6	mA
Dynamic Operating Supply Current	IDCC	50KHz, CI=1nF	--	4	8	mA
<b>Error Amplifier Section</b>						
Reference Voltage	VREF		2.46	2.5	2.57	V
Over Voltage Protection for INV Input	VINV-OVP		2.7	2.75	2.8	V
Under Voltage Protection for INV Input	VINV-UVP		0.4	0.45	0.5	V
Source Current	ICOMP	VINV=2.35V, Vcomp=1.5	7	20		uA
Sink Current	ICOMP	VINV=2.65V, Vcomp=5	10	20		uA
<b>Current Sense Section</b>						
Threshold Voltage for Peak Current Limit Cycle by Cycle Limit	VPK	Vcomp=6V	0.75	0.80	0.85	V
Propagation Delay	TPKD				200	nsec
Leading-Edge Blanking Time	TBNK	RMOT= 100K		400	500	nsec
<b>Output Section</b>						
Output Voltage Low	VOL	VDD= 12V, IO = 100mA			4	V
Output Voltage High	VOH	VDD= 12V, IO = 100mA	8			V
Rising Time	TR	VDD=12V, CL=2000pf	50	80	160	nsec
Falling Time	TF	VDD=12V, CL=2000pf	50	80	160	nsec
<b>Zero Current Detection Section</b>						
Input Threshold Voltage Rising Edge	VZVD		1.8	2.0	2.2	V
Maximum Delay from ZCD to Output Turn-On	TDEAD	Vcomp=6V, FSW=60KHz	100		400	nsec
Restart Time	TRESTART	Output Turned Off by IDET	100	120	150	usec
Inhibit Time (Max. Switching Frequency limit)	TINHIB	RMOT =100K	1.5	2.5	3	usec
<b>Maximum On-Time Section</b>						
MOT Voltage	VMOT		0.75	0.8	0.85	V
MOT Programming (resistor based)	TON-MAX	RMOT =100K, VCS=0, Comp=6V	18	20	22	usec

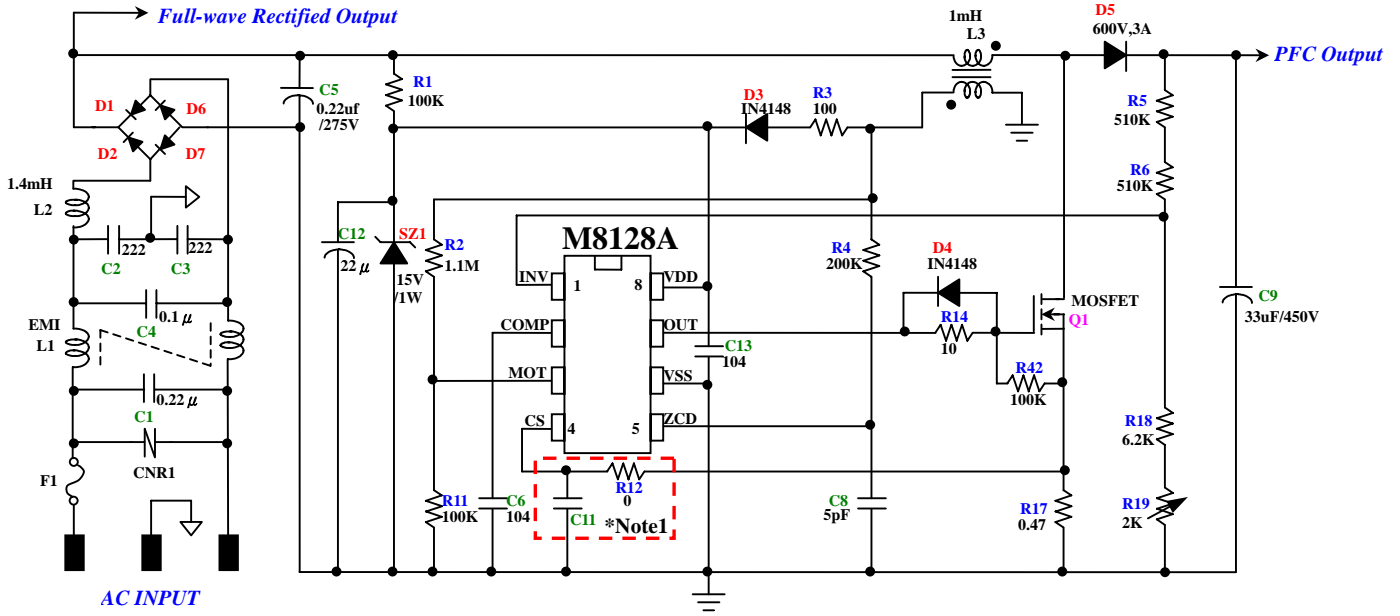


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APPLICATION DIAGRAM

(Wide-range Mains)



\* Note1 : Compensation network between CS pin and VSS



**POWER FACTOR CORRECTION CONTROLLER**

**COMPONENT LISTING**

Designator	Value	Description	Quantity
C1, C5	0.22uF	BOX Capacitor, POLYPROPYLENE FILM Capacitor	2
C4	0.1uF	BOX Capacitor, POLYPROPYLENE FILM Capacitor	1
C2, C3	222	Y Capacitor	2
C6	104	Ceramic Capacitor	1
C8	5pF	Ceramic Capacitor	1
C9	33u/450V	Electrolytic Capacitor	1
C11	OPEN	Ceramic Capacitor	1
C12	22uF	Electrolytic Capacitor	1
C13	104	Ceramic Capacitor	1
CNR1	10D511K	Varistor	1
D1, D2, D6, D7	1N4007	Diode 1000V, 1A	4
D3, D4	1N4148	Diode 75V,1A	2
D5	HER306	Diode HIGH EFFICIENCY RECTIFIERS 600V, 3A	1
F1	3.15A/250V	Fuse	1
L1		EE19 Inductor	1
L2	1.4mH	DR1012 Inductor	1
L3	1mH	EE25 Inductor	1
Q1	2SK4086	Transistor, MOSFET 600V, 11A	1
R1,R11,R42	100K	Resistor 1/4W	3
R2	1.1MEG	Resistor 1/4W	1
R3	100	Resistor 1/4W	1
R4	200K	Resistor 1/4W	1
R5, R6	510K	Resistor 1/4W	2
R12	0	Resistor 1/4W	1
R14	10	Resistor 1/4W	1
R17	0.47/1W	Resistor 1W	1
R18	6.2K	Resistor 1/4W	1
R19	VR2K	VR Resistor	1
SZ1	15V/1W	Zener Diode 1W	1
U1	M8128A	IC, Power Factor Controller	1

\* All specs and applications shown above subject to change without prior notice.

(以上電路及規格僅供參考,本公司得逕行修正)